

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Shigeki FURUYA, et al.

Serial No.: 09/922,671

Filed: August 07, 2001



Customer Number: 20277

Confirmation Number: 1398

Group Art Unit: 2815

Examiner: Matthew E. WARREN

For: CMOS BASIC CELL AND METHOD FOR FABRICATING  
SEMICONDUCTOR INTEGRATED CIRCUIT USING THE SAME

Mail Stop FEE Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

☐  
☐  
☒

No additional fee is required.

Applicant is entitled to small entity status under 37 CFR 1.27

Also attached: Petition for Extension of Time

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	35	30	5	\$18.00 =	\$90.00
Independent Claims	6	5	1	\$86.00 =	\$86.00
Multiple claims newly presented					\$0.00
Fee for extension of time					\$110.00
					\$0.00
Total of Above Calculations					\$286.00

- ☒ Please charge my Deposit Account No. 500417 in the amount of \$286.00. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

MCDERMOTT, WILL &amp; EMERY



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